

Substitute for form 1449A/PTO				Complete if Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Application Number	10/676,883
				Filing Date	9/30/03
				First Named Inventor	Christopher L. Rumer
				Art Unit	2891
				Examiner Name	C. Everhart
				Attorney Docket Number	42P16840
Sheet	1	of	1		

## **NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No.*	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T*
<i>CME</i>		RECHE, J., "Wafer Thinning and Thru-Silicon Vias", The Path to Wafer Level Packaging, 41 pages, Tru-Si Technologies, <a href="http://www.trusi.com">www.trusi.com</a> , IEEE/CPMT Meeting, Santa Clara (CA)	
		05/10/2000.	

Examiner Signature	<i>C. Jauckart</i>	Date Considered	7-20-05
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

*\*Applicant's unique citation designation number. \*Applicant is to place a check mark here if English language Translation is attached.*

Based on PTO/SB/088 (08-03) as modified by Blakely, Solokoff, Taylor & Zaifman (wtr) 08/11/2003.  
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